IRIG Corruptor Commands

## General

|  |  |  |  |
| --- | --- | --- | --- |
| Command | Parameters/Input format | Description | Example |
| # | Any. | Comment | # Let’s hose some IRIG! |
| // | Any. | Comment | // No me gusta lechuga |
| WAIT | Time in ms to wait. | Halts execution of the script until the specified number of milliseconds pass. | WAIT 1000 (waits 1 second) |
| OUT\_OFF | None. | The port’s IRIG output will be shut off. | OUT\_OFF |
| OUT\_ON | None. | The port’s IRIG output will be turned on. | OUT\_ON |
| RESET | None. | Resets the port’s IRIG Corruptor to initial values. | RESET |

## For setting time and IRIG control bits

|  |  |  |  |
| --- | --- | --- | --- |
| Command | Parameters/Input format | Description | Example |
| Time | hh:mm:ss | Sets the time. | TIME 4:57:23 |
| Date | mm/dd/yyyy or mm/dd/yy | Sets the date. | DATE 3/12/12 |
| UTC | (+-)nn.n | Sets the UTC offset. | UTC -08.5 |
| TQUAL | 0 – 0ms  1 – 1ns  2 – 10ns  3 – 100ns  4 – 1us  5 – 10us  6 – 100us  7 – 1ms  8 – 10ms  9 – 100ms  10 – 1s  11 – 10s  15 – FAULT | Sets the time quality bits. | TQUAL 6 |
| DSTP | 1 or 0 | Sets the DSTP bit | DSTP 0 |
| DST | 1 or 0 | Sets the DST bit | DST 1 |
| LS | 1 or 0 | Sets the leap second bit | LS 0 |
| LSP | 1 or 0 | Sets the leap second pending bit | LSP 1 |

Table : Scripting commands for time

## For setting jitter

|  |  |  |  |
| --- | --- | --- | --- |
| Command | Parameters/Input format | Description | Example |
| PPS\_JIT | Jitter value in ns, up to 10ms. Non-negative values only. | Sets jitter on pps pulses. | PPS\_JIT 500 |
| 10MS\_JIT | Jitter value in ns, up to 10ms. Non-negative values only. | Sets jitter on 10ms pulses. | 10MS\_JIT 1000 |
| JITTER | 1 or 0. | Enables or disables output jitter. | JITTER 1 |

Table : Scripting commands for jitter

## For forcing output (missing pulses, extra pulses)

|  |  |  |  |
| --- | --- | --- | --- |
| Command | Parameters/Input format | Description | Example |
| FORCE\_TRIG | 0 – Trigger on FORCE\_START command.  1 – Trigger on next PPS after FORCE\_START command.  2 – Trigger on next 10ms rising edge after FORCE\_START command. | Sets the trigger that will begin the force output sequence. | FORCE\_TRIG 2 |
| FORCE\_MOD | 0 – No modification  1 – Force output IRIG to 1.  2 – Force output IRIG to 0.  3 – Force output IRIG to the inverse of the input IRIG. | Sets the modification that will be performed by the force output sequence. | FORCE\_MOD 3 |
| FORCE\_DELAY | Delay in ns. Range is 0 to 0.214 seconds. | Sets the amount of time to wait after the trigger before modifying the output IRIG signal. | FORCE\_DELAY 1000 |
| FORCE\_DUR | Duration in ns. Range is 0 to 0.214 seconds. | Sets the amount of time to spend modifying the output IRIG signal. | FORCE\_DUR 555 |
| FORCE\_START | None. | Begin waiting for trigger event. | FORCE\_START |
| FORCE\_STOP | None. | Stop everything and return to passing input IRIG through to output unmodified. | FORCE\_STOP |